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**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Hideharu ITATANI et al.

Group Art Unit: 2895

Application No.: 10/521,248

Docket No.: 122430

Filed: April 11, 2005

For: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE AND  
SUBSTRATE PROCESSING APPARATUS

**REQUEST FOR CORRECTION OF PALM RECORDS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records.

Respectfully submitted,

James A. Oliff  
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JAO:MAC/clt

Date: February 27, 2009

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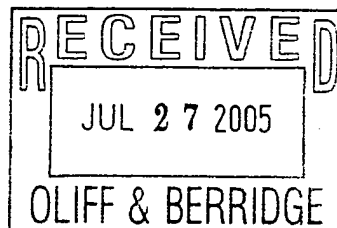
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/521,248	04/11/2005	2812	1030	122430	6	20	3

CONFIRMATION NO. 9181

25944  
 OLIFF & BERRIDGE, PLC  
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 ALEXANDRIA, VA 22320



## FILING RECEIPT



\*OC000000016561538\*

Date Mailed: 07/22/2005

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## Applicant(s)

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## Assignment For Published Patent Application

HITACHI KOKUSAI ELECTRIC INC., TOKYO, JAPAN

**Power of Attorney:** The patent practitioners associated with Customer Number 25944.

## Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP03/08964 07/15/2003

## Foreign Applications

JAPAN 2002-205627 07/15/2002

Projected Publication Date: 10/27/2005

Non-Publication Request: No

Early Publication Request: No

**Title**

Method for manufacturing semiconductor device and substrate processing apparatus

**Preliminary Class**

438

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